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SEP 14 2005  
P A T E N T & T R A D E M A R K S E C T I O N  
Attorney Docket No.: 80398.P348

Patent

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

Hideakzu Watanabe

Serial No.: 09/802,417

Filed: March 8, 2001

For: Effective Bus Utilization Using Multiple  
Bus Interface Circuits and Arbitration  
Logic Circuit

Mail Stop Issue Fee  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Examiner: Auve, G.  
Art Unit: 2111

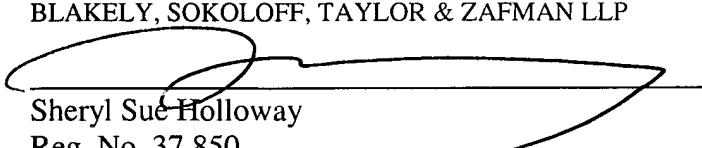
**COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE**

Applicants are assuming that the Examiner's statement of reasons for allowance is to be taken in light of the structure and interaction recited in the claims. Applicants note that the Examiner's various comments should not be used to read non-existent limitations into the claims.

Respectfully submitted,

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Date: Sept. 12, 2005

  
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